

**Amendments to the Specification:**

On page 1, please amend paragraph [0001] as follows:

[0001] The present application is related to U.S. Patent Application Serial No. 10/689,335 [[\_\_\_\_\_]] entitled "Method for Finding Local Extrema of a Set of Values for a Parallel Processing Element" filed [[\_\_\_\_\_]] ~~(DB001076-000, Micron no. 03-0052)~~ 20 October 2003, and U.S. Patent Application Serial No. 10/689,256 [[\_\_\_\_\_]] entitled "Method for Finding Global Extrema of a Set of Bytes Distributed Across an Array of Parallel Processing Elements" filed [[\_\_\_\_\_]] ~~(DB001077-000, Micron no. 03-0053)~~ 20 October 2003.

On pages 10 – 11, please amend paragraph [0054] as follows:

[0054] For example in the current embodiment, each PE in array 28 (see FIG. 7) receives a set of values from the DRAM interface 44 and the host memory access port 46 (see FIG. 1), among others. After the values are assigned to each PE in the array 28, each PE determines its local extrema. In the current embodiment, local extrema refers to the maximum or minimum value for a set of values assigned to an individual PE. A method for determining a local extrema from a set of values on an individual PE is discussed in more detail in U.S. Patent Application Serial No. 10/689,335 [[\_\_\_\_\_]] entitled "Method for Finding Local Extrema of a Set of Values for a Parallel Processing Element" filed [[\_\_\_\_\_]] ~~(DB001076-000, Micron no. 03-0052)~~ 20 October 2003 and incorporated in its entirety by reference herein.